PTO/SB/21 (09-04) Approved for use through 07/31/2006. OMB 0651-0031 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE ction of information unless it displays a valid OMB control number. erwork Reduction Act of 1995, no persons are required to respond to a co **Application Number** 10/724,996 Filing Date TRANSMITTAL December 01, 2003 First Named Inventor **FORM** Hils. Andreas Art Unit 2825 **Examiner Name** Siek, Vuthe (to be used for all correspondence after initial filing) Attorney Docket Number 03-0352 Total Number of Pages in This Submission **ENCLOSURES** (Check all that apply) After Allowance Communication to TC Fee Transmittal Form Drawing(s) Appeal Communication to Board Licensing-related Papers Fee Attached of Appeals and Interferences Appeal Communication to TC Petition (Appeal Notice, Brief, Reply Brief) Amendment/Reply Petition to Convert to a **Proprietary Information** After Final Provisional Application Power of Attorney, Revocation Status Letter Affidavits/declaration(s) Change of Correspondence Address Other Enclosure(s) (please Identify **Terminal Disclaimer Extension of Time Request** below): Request for Refund **Express Abandonment Request** 1. Copy of Cited References CD, Number of CD(s) Information Disclosure Statement 2. Return Postcard Landscape Table on CD Certified Copy of Priority Remarks Document(s) - IDS Filing Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Name LSI Logic Corporation Signature

	CERTIFICATE OF TRA	ANSMISSION/MAILING			
	orrespondence is being facsimile transmitted to class mail in an envelope addressed to: Com				
Signature	mildel				
Typed or printed name	Mark Salvatore		Date	2-24-06	

Reg. No.

36,771

Printed name

Date

Timothy R. Croll

22 FEB06

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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	For FY	2006		First Named Inv	entor	Hils, And			
				Examiner Name	•	Siek, Vutl	he		
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METHOD OF	PAYMENT (check	all that apply)							
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FEE CALCUL	ATION (All the fe	es below are du	e upon f	iling or may be	subjec	t to a sur	charge.)		
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Plant	200	100	300	150	160	0 8	0		
Reissue	300	150	500	250	600	0 30	0		
Provisional	200	100	0	0	(0	0		
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sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s). Total Sheets Extra Sheets Number of each additional 50 or fraction thereof - 100 = / 50 = (round up to a whole number) x =									
4. OTHER FEE(S) Non-English Specification, \$130 fee (no small entity discount) Fees Paid (\$)				ees Paid (\$)					
Other (e.g.,	late filing surchar	ge): <u>IDS Filing</u>							180
SUBMITTED BY									
Signature	1 TRLD			Registration No.	36,771		Telephone	e (408) 43:	3-7625

SUBMITTED BY			
Signature	12th ha	Registration No. (Attorney/Agent) 36,771	Telephone (408) 433-7625
Name (Print/Typ	pe) Timothy R. Croll		Date 72 F&B 66

This collection of information is required by 37 CFR 1.136. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of:

Andreas Hils et. al.

Serial No.:

10/724,996

Filed:

December 01, 2003

For:

Integrated Circuits, and Design and

Manufacture Thereof

10724996

§ § I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450,

Alexandria, VA 22313-1450, on the date below: Mark Salvatore

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Siek, Vuthe

1496.00327 / 03-

Signature

Group Art Unit:

Examiner:

Atty Docket:

03/01/2006 HTECKLU1 00000025 122252 01 FC:1806

180.00 DA

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035

408-433-7475

Respectfully submitted,

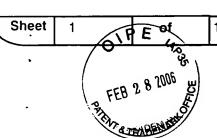
Timothy Croll

Reg. No. 36,771



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

	Complete if Known
Application Number	10/724,996
Filing Date	December-01, 2003
First Named Inventor	Andreas Hils
Group Art Unit	2825
Examiner Name	Vuthe Siek
Attorney Docket No.	1496.00327 / 03-0352



	OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, caalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Т		
		"Submicron CMOS Gate Array", IBM Technical Disclosure Bulletin, Volume 33, No. 3B, August 1990, pages 129-131			
		"An Integrated Design Flow for a Via-Configurable Gate Array", Ran et al., Computer Aided Design, 2004, ICCAD-2004, IEEE/ACM International Conference, November 7-11, 2004, pages 582-589			
		"Structured ASICs: Opportunities and Challenges", Zahiri, Proceedings 2003 IEEE International Conference on Computer Design: VLSI in Computers and Processors, ICCD 2003, October 13-15, 2003, pages 404-409			

Examiner	Date
signature	considered